



**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:**

Raymond J. Beffa

**Serial No.:** 10/600,148

**Filed:** June 19, 2003

**For:** METHOD IN AN INTEGRATED  
CIRCUIT (IC) MANUFACTURING  
PROCESS FOR IDENTIFYING AND  
REDIRECTING IC'S MIS-PROCESSED  
DURING THEIR MANUFACTURE

**Confirmation No.:** 6251

**Examiner:** P. Assouad

**Group Art Unit:** 2857

**Attorney Docket No.:** 2269-3040.5US  
(96-0970.05/US)

**Notice of Allowance Mailed:**

February 28, 2005

**NOTICE OF EXPRESS MAILING**

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Date of Deposit with USPS: May 26, 2005

Person making Deposit: Steve Wong

**AMENDMENT PURSUANT TO 37 C.F.R. § 1.312(a)**

Mail Stop ISSUE FEE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

Please amend the above-referenced application as follows:

**Amendments to the Specification** appear on page 3 of this paper.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 4 of this paper.

**Amendments to the Drawings** appear on page 12 of this paper and include both an attached replacement sheet and an annotated sheet showing changes.

**Remarks** begin on page 13 of this paper.

An **Appendix** including amended drawing figures is attached following page 13 of this paper.